


[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

 Search: ☒ The ACM Digital Library ☐ The Guide
 +emulate +instruction +push +pop +stack +pointer

[Feedback](#) [Report a problem](#) [Satisfaction survey](#)
Terms used: **emulate instruction push pop stack pointer**Found **131** of **213,097**

Sort results by relevance

Display results expanded form

[Save results to a Binder](#) [Search Tips](#)☐ Open results in a new windowTry an [Advanced Search](#)Try this search in [The ACM Guide](#)

Results 1 - 20 of 131

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [next](#)Relevance scale ☐ ☐ ☐ ☐ ☐**1** [Shared memory objects: An almost non-blocking stack](#)

Hans-J. Boehm

July 2004

Proceedings of the twenty-third annual ACM symposium on Principles of distributed computing PODC '04**Publisher:** ACM PressFull text available: [pdf\(174.83 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Non-blocking data structure implementations can be useful for performance and fault-tolerance reasons. And they are far easier to use correctly in a signal- or interrupt-handler context. We describe a weaker class of "almost non-blocking" data structures, which block only if more than some number N of threads attempt to simultaneously access the same data structure. We argue that this gives much of the benefit of fully non-blocking data structures, particularly for signal or interrupt hand ...

Keywords: compare-and-swap, interrupt handler, linked list, lock-free, memory allocation, non-blocking, signal handler, stack

2 [Improving prediction for procedure returns with return-address-stack repair mechanisms](#)

Kevin Skadron, Pritpal S. Ahuja, Margaret Martonosi, Douglas W. Clark

November 1998

Proceedings of the 31st annual ACM/IEEE international symposium on Microarchitecture MICRO 31**Publisher:** IEEE Computer Society PressFull text available: [pdf\(1.66 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**3** [Hardware and Binary Modification Support for Code Pointer Protection From Buffer Overflow](#)

Nathan Tuck, Brad Calder, George Varghese

December 2004

Proceedings of the 37th annual IEEE/ACM International Symposium on Microarchitecture MICRO 37**Publisher:** IEEE Computer SocietyFull text available: [pdf\(294.15 KB\)](#)Additional Information: [full citation](#), [abstract](#), [citations](#)

Buffer overflow vulnerabilities are currently the most prevalent security vulnerability; they are responsible for over half of the CERT advisories issued in the last three years. Since

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	185	(emulat\$3 with instruction) and ((push and pop) with instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L2	1	(emulat\$3 with instruction) and ((push1 and pop1) with instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L3	185	(emulat\$3 with instruction) and ((push and pop) with instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L4	184	L3 and stack	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L5	175	L4 and pointer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L6	11	(emulat\$3 with (push and pop) with instruction\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L7	11	L6 and pointer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L8	11	L7 and (location or address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58

EAST Search History

L9	13	"6009515"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L10	34	L5 and (instrument\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L11	34	L10 and (interrupt or trap or probe)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L12	4	L6 and (stack with pointer\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L13	224	stack and eip and ebp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L14	99	L13 and emulat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L15	67	L14 and push and pop	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L16	38	L15 and (emulat\$3 with instruction\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58

EAST Search History

L17	4951	emulat\$3 with instruction\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L18	2727	stack adj frame	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L19	252	L18 and L17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L20	27	L19 and (base adj pointer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L21	33	L19 and (ebp and esp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L22	4	L21 and (emulat\$3 with (push or pop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L23	4	L22 and (trap adj handler)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L24	4	L23 and (trap adj instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58

EAST Search History

L25	4	L24 and (transfer\$3 near3 control)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L26	33	L19 and ebp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L27	4	L24 and (transfer\$3 near3 control near3 trap)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L28	12	(bryan near1 cantrill).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L29	224	stack and eip and ebp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L30	2727	stack adj frame	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L31	4951	emulat\$3 with instruction\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L32	252	L30 and L31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58

EAST Search History

L33	33	L32 and (ebp and esp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L34	4	L33 and (emulat\$3 with (push or pop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L35	4	L34 and (trap adj handler)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L36	4	L35 and (trap adj instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L37	4	L36 and (transfer\$3 near3 control)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L38	185	(emulat\$3 with instruction) and ((push and pop) with instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L39	184	L38 and stack	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L40	175	L39 and pointer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58

EAST Search History

L41	27	L32 and (base adj pointer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L42	33	L32 and ebp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L43	12	(bryan near1 cantrill).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L44	1	(emulat\$3 with instruction) and ((push1 and pop1) with instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L45	11	(emulat\$3 with (push and pop) with instruction\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L46	4	L45 and (stack with pointer\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L47	34	L40 and (instrument\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L48	11	L45 and pointer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58

EAST Search History

L49	4	L36 and (transfer\$3 near3 control near3 trap)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L50	11	L48 and (location or address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L51	99	L29 and emulat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L52	67	L51 and push and pop	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L53	38	L52 and (emulat\$3 with instruction\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L54	13	"6009515"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L55	185	(emulat\$3 with instruction) and ((push and pop) with instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58
L56	34	L47 and (interrupt or trap or probe)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/26 10:58